

9. An image display according to claim 1,
wherein the display pixel array is a liquid crystal
display pixel array using a TN (Twisted Nematic) mode
liquid crystal.

5 10. An image display which displays image data on
an image display part constructed by a display pixel
array, wherein an image data input circuit inputs at
least one moving image data and at least one still
10 image data at different frame rates (> 0) into the
image display part by selecting addresses in a row
direction and a column direction of the display pixel
array;

 wherein the display pixel array includes row
direction address lines and column direction address
15 lines; and

 wherein display pixels of the display pixel
array each include an AND functional circuit which is
connected to one of the row direction address lines and
one of the column direction address lines.

20 11. An image display according to claim 10,
wherein the moving image data is inputted into the
image display part in a real-time manner from
generation of data.

12. An image display according to claim 10,
further comprising a still image data storing circuit
which temporarily stores the still image data until it
is inputted into the image display part.

5 13. An image display according to claim 12,
further comprising a code data storing circuit which
temporarily stores two-gradation text and figure data
in a predetermined code data format until it is
inputted into the image display part.

10 14. An image display which displays image data on
an image display part constructed by a display pixel
array, wherein image data input means inputs image data
into the image display part by selecting addresses in a
row direction and a column direction of the display
15 pixel array so that the display pixel array has two
neighboring areas having different frame rates (> 0);

wherein the display pixel array includes row
direction address lines and column direction address
lines; and

20 wherein display pixels of the display pixel
array each include an AND functional circuit which is
connected to one of the row direction address lines and
one of the column direction address lines.

15. An image display which displays image data on an image display part constructed by a display pixel array, wherein image data input means inputs at least one moving image data and at least one still image data at different frame rates (> 0) into the image display part by selecting addresses in a row direction and a column direction of the display pixel array;

wherein the display pixel array includes row direction address lines and column direction address lines; and

wherein display pixels of the display pixel array each include an AND functional circuit which is connected to one of the row direction address lines and one of the column direction address lines.